

a plurality of switchers, provided for the pixel electrodes, for supplying signals from the data lines to the pixel electrode;

a gate line driver for scanning the gate lines;

a data line driver for driving the data lines, in accordance with the gradation to be displayed; and

a controller for controlling the gate line driver and the data line driver, wherein

a' concl.
the controller comprises a signal absence detector for detecting that at least one of a video signal, a horizontal synchronization signal, and a vertical synchronization signal is no longer being input to the liquid crystal display device, and

wherein, in response to a detection by the signal absence detector that one of the signals is no longer being input, the controller outputs a signal to the gate line driver to make all the gate lines active for a predetermined time and the controller outputs a signal to the data line driver to supply the same electric potential as applied to the common electrode to all the data lines for the predetermined time.--

Cancel claim 3.

Amend claim 7 as follows:

a2
SUBB7
--7. (amended) A method for controlling a liquid crystal display device comprising: pixel electrodes; a common electrode; a plurality of data lines and a plurality of gate